

PAT-NO: **JP411065969A**

DOCUMENT-IDENTIFIER: **JP 11065969 A**

TITLE: **SERVER EQUIPMENT, COMMUNICATION
CONNECTING METHOD AND
RECORDING MEDIUM RECORDING PROGRAM FOR
CONNECTING
COMMUNICATION**

PUBN-DATE: **March 9, 1999**

INVENTOR-INFORMATION:

NAME

ASANO, SHIGEHIRO

KANAI, TATSUNORI

SUGANO, SHINICHI

MAEDA, SEIJI

ASSIGNEE-INFORMATION:

NAME

COUNTRY

TOSHIBA CORP

N/A

APPL-NO: **JP09222105**

APPL-DATE: **August 19, 1997**

INT-CL (IPC): **G06F013/00**

ABSTRACT:

PROBLEM TO BE SOLVED: To expand the data sending out capability of Web data to a network by adding a network card consisting of a processor, a memory, a network interface part to a general purpose computer to expand a memory band width.

SOLUTION: A host CPU 2, a cash memory 3 and a main storage memory 4 are connected through a PCI(peripheral component interconnect) bus 1a on a host side. In addition, the PCI bus 1a on this host side is connected with another PCI bus through a host/PCI bridge 5. A network controller 6, network interface processors 7a, 7b,..., and SCSI controller 8, etc., are connected to this PCI bus. The controller 6 is provided in the form of a card to be loaded to a main device, e.g. The capability of a Web server is expanded at a comparatively low cost.

COPYRIGHT: (C)1999,JPO

PUB-NO: EP001003314A2

DOCUMENT-IDENTIFIER: EP 1003314 A2

TITLE: Improved totem communications system and
method

PUBN-DATE: May 24, 2000

INVENTOR-INFORMATION:

NAME	COUNTRY
MINYARD, TRENTON C	US
STOVALL, GREGORY T	US

ASSIGNEE-INFORMATION:

NAME	COUNTRY
NORTEL NETWORKS CORP	CA

APPL-NO: EP99309171

APPL-DATE: November 18, 1999

PRIORITY-DATA: US19506798A (November 18, 1998)

INT-CL (IPC): H04L029/06, H04L012/433

EUR-CL (EPC): H04L029/06 ; H04L029/06

ABSTRACT:

**CHG DATE=20001128 STATUS=0> An improvement is disclosed
for a Totem system
having a network and a plurality of host processors connectable to
the network,
each of which host processors includes a CPU and is configured
for executing
processes, wherein the improvement includes, for each host
processor, a buffer
memory and a co-processor for each host processor. The buffer
memory is
electrically connected to the CPU and configured for storing
messages sent to
or from the CPU. The co-processor is electrically connected for
providing an
interface between the network and the host processor, and is
configured for
responding to tokens and for delivering messages from the
network to the buffer
memory for retrieval by the CPU, and for delivering to the network
messages
stored in the buffer memory by the CPU. <IMAGE>**